## VNR VIGNANA JYOTHI INSTITUTE OF ENGINEERING AND TECHNOLOGY

Vignana Jyothi Nagar, Pragathi Nagar, Nizampet (S.O), Hyderabad – 90

## TIMETABLE FOR Ph.D. COURSE WORK, II SEMESTER (R22), SESSIONAL EXAMINATION-I - MAY, 2025

Branch	12 <sup>th</sup> May 2025 10:00 AM to 12:00 NOON	13 <sup>th</sup> May 2025 10:00 AM to 12:00 NOON	14 <sup>th</sup> May 2025 10:00 AM to 12:00 NOON	15 <sup>th</sup> May 20254 10:00 AM to 12:00 NOON	16 <sup>th</sup> May 2025 10:00 AM to 12:00 NOON	17 <sup>th</sup> May 2025 10:00 AM to 12:00 NOON
	Monday	Tuesday	Wednesday	Thursday	Friday	Saturday
EEE	Advanced Power Electronic Converters (22PC1PL04)	Digital Controllers in Power Electronic Applications (22PC1PL05)	Digital Protection of Power System (22PC1PS04)	Power Quality (22PC1PS05)	×	ক্ষ
ECE	34e	Machine Learning (22PC1CP03)	IoT Architecture and System Design (22PC1ES04)	Image and Video Processing (22PE1ES08)	Low Power VLSI Design (22PE1VS11))	÷.

**Controller of Examinations** 

02.05.2005

Copy to :All HOD's

**Note:** 1. ANY OMISSIONS OR CLASHES IN THIS TIME TABLE MAY PLEASE BE INFORMED TO THE EXAMINATION BRANCH IMMEDIATELY 2. EVEN IF GOVERNMENT DECLARES HOLIDAY ON ANY OF THE ABOVE DATES, THE EXAMINATIONS SHALL BE CONDUCTED AS PER SCHEDULE.